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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

SHINOZAKI

Art Unit: 2816

Application No.: 09/385,014

Examiner: D. LE

Filed: August 27, 1999

Attorney Dkt. No.: 108075-09014

For: INPUT CIRCUIT HAVING CURRENT REGULATING TRANSISTOR

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
Washington, D.C. 20231

Date: March 27, 2002

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TECHNOLOGY CENTER 2800

Sir:

Prior to initial examination of the application, please amend the above-identified application as follows:

**IN THE CLAIMS:**

Please cancel claim 3 without prejudice.

Please amend claims 1, 4 and 5 as follows. Pursuant to 37 C.F.R. § 1.121, as amended, a copy of the marked-up version of original claims 1, 4 and 5 is attached to this Amendment showing the changes made therein.

1. (Three Times Amended) An input circuit comprising:

a current mirror circuit including a self-biased transistor and a non-self-biased transistor connected to each other;

a differential circuit including a first transistor a first drain of which is connected to the non-self-biased transistor for receiving an external signal and a second transistor a

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